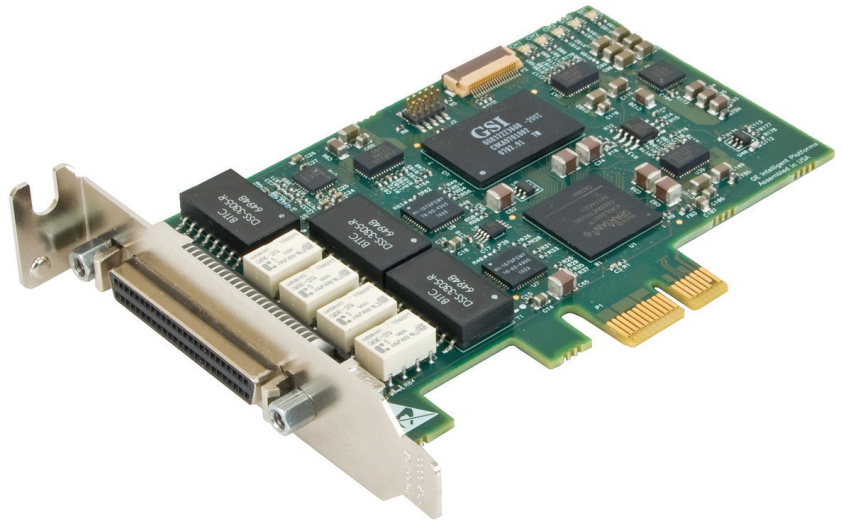




R15-LPCIE

High Density Low Profile PCI Express Module

Abaco Systems' R15-LPCIE is the latest generation of performance and flexibility for MIL-STD-1553A/B Notice II on a low profile PCI Express module. Available in -40°C to +70°C temperatures with one or two dual-redundant channels, the R15-LPCIE includes advanced API (Application Programming Interface) software that reduces application development time.

Standard features include: IRIG-B signal Receiver/Generator with GPS synchronization, software selectable transformer or direct coupling, variable voltage output, 66 MHz, PCI bus support, 1 Mbyte of RAM per channel, 64-bit 25 nanosecond message time-tagging, dedicated input and output triggers for each 1553 channel, extensive BC & RT link-list structures, error injection/detection, 12 bi-directional avionics level discretes, automatic/manual RT Status Bit and Mode Code responses, along with advanced BC functionality.

The R15-LPCIE Bus Monitors provide unparalleled error detection and 100%

monitoring of fully loaded buses.

Multi-function Interfaces

R15-LPCIE multi-function interfaces are easily configured to operate with simultaneous Bus Controller, 31 Remote Terminals and Bus Monitor functionality.

Dual-function Interfaces

Dual-function R15-LPCIE interfaces have all the features and functionality of the multi-function versions, with either Bus Monitor and Bus Controller or Bus monitor and 31 Remote terminals.

Software

Abaco provides our advanced 1553 API in source code, along with support for Microsoft Windows®, VxWorks® and Linux (contact the factory for supported operating system versions). To access 1553 functionality without software development, BusTools/1553, Abaco's MIL-STD-1553 bus analysis, simulation and data logging/monitoring solution is available.

FEATURES:

- 1 or 2 dual-redundant MIL-STD-1553A/B Notice II channels
- Native 1 lane PCI Express interface (no bridge)
- Simultaneous Bus Controller, 31 Remote Terminals and Bus Monitor
- High-level API for Windows®, VxWorks® and Linux included (contact the factory for supported operating system versions)
- Multi- and Dual-function versions
- Standard IRIG-B receiver/generator
- 64-bit, 25 nanosecond time-tagging (virtually unlimited time stamping)
- Complete message programmability
- Flexible message status/interrupt generation
- I/O triggering and error injection/detection
- External differential time-tag reset and clock inputs
- 12 Bi-directional Avionics discretes
- 2 Differential I/O channels
- Dedicated input and output triggers for each 1553 channel
- Host readable on board temperature sensor
- External RT addressing
- Transition cabling to 1553 cable jacks optional
- Optional conformal coating
- RoHS compliant to EU directive 2002/95/EC

R15-LPCIE *High Density Low Profile PCI Express Module*

Specifications

Physical

- PCI Express Low Profile Interface Card (2.535 x 4.000 inches)

Environmental

- Standard operating temperature range: -40° C to +70° C in Free Air and -40° C to +75° C with 600 ft. /min airflow
- Relative humidity: 5 to 95% (non-condensing)
- Optional conformal coating configurations

Software Support

- API - High-level libraries with source code included for Windows®, VxWorks® and Linux (contact the factory for supported operating system versions)
- GUI - Optional BusTools/1553 GUI Bus Analysis, Simulation & Data Logging software

Connections

- Software selectable transformer or direct coupling via relays
- 2 Differential I/O channels
- Dedicated input and output Triggers for each 1553 channel
- Transition cabling to 1553 cable jacks optional

Multi-function Operational Modes

- Simultaneous BC, 31 RTs and BM

Dual-function Operational Modes

- BM and 31 RTs or BM and BC

Power (2 channels at 75% duty cycle)

- +12 VDC @ 430mA (typ.)
- +3.3 VDC @ 230A (typ.)
- 4.5 W power dissipated on board

On-board Shared RAM

- 1 MByte (per dual-redundant channel)

Timing

- Independent 64-bit 25 nanosecond message time-tagging per channel
- Time can be programmed via the host or by a 1553 trigger
- All timers can be synchronized to 0 via the host
- Timers can independently use IRIG time
- IRIG-B receiver (AM or DC/TTL) and generator (DC/TTL)

Optional Configurations

- 1 or 2 dual-redundant channels
- 1 or 2 Dual or Multi function channels
- Optional conformal coating
- Optional Transformer or Direct coupling
- Optional full height face plate
- Contact factory for Custom requirements

Bus Controller

- Programmable control over:
 - Major and minor frame content and timing
 - Intermissive gap times
 - Response time-out and late response
- Modify messages, data or setup on the fly
- Multiple data buffers
- Insert aperiodic messages into a running BC list
- Conditional message sequencing based on real-time message data or status
- Selectable interrupt generation and status messages on full range of system conditions or all detected errors
- Synchronize BC operation to external time source
- 64-bit 25 nanosecond resolution time tagging on BC, RT and BM
- Virtually unlimited time stamping

Programmable full error detection/injection available on BC and RT and full detection available on BM (on a per word basis)

- | | |
|-------------------|------------------------|
| • Invalid word | • Late response |
| • Bit count error | • Early response |
| • High word | • No response |
| • Low word | • Incorrect RT address |
| • Inverted sync | • Parity error |
| • Manchester | |

Remote Terminal

- Multiple RT simulation (up to 31 RTs)
- Modify data, status words or setup while card is running
- Programmable message content (linked message buffers)
- Selectable interrupts upon multiple conditions
- RT Map Monitoring

Bus Monitor

- Capture 100% fully loaded bus traffic with:
 - Time-tagging
 - Error status
 - Word status
 - Message status
- Interrupts can be selected by RT / SA / WC
- Extensive filtering and triggering options
 - By individual RT/subaddress
 - Transmit, receive or broadcast mode codes
 - Internal or external triggering
 - Trigger output on user specified data
- Real-time bus playback with RT edit mode



R15-LPCIE High Density Low Profile PCI Express Module

Ordering information

R15-LPCIE-G2-1D	MIL-STD-1553 Dual-function, Single dual-redundant channel, 32-bit FW, Low profile PCI Express interface board, 12 bi-directional discretes, Relay coupled, Variable voltage with IRIG-B Rec/Gen
R15-LPCIE-G2-1M	MIL-STD-1553 Multi-function, Single dual-redundant channel, 32-bit FW, Low profile PCI Express board, 12 bi-directional discretes, Relay coupled, Variable voltage with IRIG-B Rec/Gen
R15-LPCIE-G2-2D	MIL-STD-1553 Dual-function, Two dual-redundant channel, 32-bit FW, Low profile PCI Express board, 12 bi-directional discretes, Relay coupled, Variable voltage with IRIG-B Rec/Gen
R15-LPCIE-G2-2M	MIL-STD-1553 Multi-function, Two dual-redundant channel, 32-bit FW, Low profile PCI Express board, 12 bi-directional discretes, Relay coupled, Variable voltage with IRIG-B Rec/Gen

Legacy part numbers are still available. Contact your local sales representative for more information.

Optional Hardware

-K suffix	Conformal coating
-F suffix	Full height face plate
-R suffix	Ruggedized, extended temperature, and hardwired for transformer coupling only
-CBL suffix	Includes transition cable

Optional Software

BT-1553	MIL-STD-1553 Bus Analysis, Simulation & Data Logging software for Windows
LV-1553	LabVIEW support for MIL-STD-1553

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